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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Low, *et al.* Docket No.: 02 P 15173 US / INTECH  
3.0-076  
Serial No.: 10/689,233 Art Unit: 2891  
Filed: October 20, 2003 Examiner: Asok K. Sarkar  
For: Inclusion Of Low-K Dielectric Material Between Bit Lines

**Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))**

Date of Deposit: November 23, 2005

I hereby certify that the below listed correspondence is being deposited with the United States Postal Service on the date indicated above as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Certificate of Mailing via First Class Mail (1 page)  
Amendment (14 pages)  
Replacement Formal Drawing Sheets (11 pages)  
Two (2) Return Postcards

Respectfully submitted,

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